L Number	Hits	Search Text	DB	Time stamp
5	52	(((dma (direct adj2 memory adj2 access)) same register\$3) and (fifo	USPAT;	2003/10/16 10:18
		same (state adj2 machine)) and (memor\$3 same interfac\$3 same (dma	US-PGPUB;	
		(direct adj2 memory adj2 access)) and external) and (((cpu processor)	EPO; JPO;	
		same ((control\$4 near4 register) and (status near4 register) and (dma	DERWENT;	
		(direct adj2 memory adj2 access))) and (dsp (digital adj2 signal adj2 processor)))))	IBM_TDB	
6	0	((((dma (direct adj2 memory adj2 access)) same register\$3) and (fifo	USPAT;	2003/10/16 10:51
٠	ŭ	same (state adj2 machine)) and (memor\$3 same interfac\$3 same (dma	US-PGPUB;	
		(direct adj2 memory adj2 access)) and external) and (((cpu processor)	ЕРО; ЛРО;	
		same ((control\$4 near4 register) and (status near4 register) and (dma	DERWENT;	
		(direct adj2 memory adj2 access))) and (dsp (digital adj2 signal adj2	IBM_TDB	
		processor))))) and ((ping-pong (ping adj2 pong)) with memory)		
7	52	((real-time (real adj2 time)) same (dsp (digital adj2 signal adj2	USPAT;	2003/10/16 10:19
		processor)) same load\$3 same manag\$5)	US-PGPUB;	
			ЕРО; ЈРО;	
			DERWENT;	
	1		IBM_TDB	2002/10/17 10 20
8	3	(((real-time (real adj2 time)) same (dsp (digital adj2 signal adj2	USPAT;	2003/10/16 10:20
		processor)) same load\$3 same manag\$5)) and ((ping-pong (ping adj2	US-PGPUB;	
		pong)) with memory)	EPO; JPO; DERWENT;	
			IBM_TDB	
	274	((dsp (digital adj2 signal adj2 processor)) same load\$3 same manag\$5)	USPAT;	2003/10/16 10:20
9	274	((dsp (digital adj2 signal adj2 processor)) same road\$3 same manag\$3)	US-PGPUB;	2003/10/10 10:20
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
10	7	(((dsp (digital adj2 signal adj2 processor)) same load\$3 same manag\$5)	USPAT;	2003/10/16 10:22
) and ((ping-pong (ping adj2 pong)) with memory)	US-PGPUB,	
)	ЕРО; ЈРО;	
			DERWENT;	
			IBM_TDB	
11	4	((((dsp (digital adj2 signal adj2 processor)) same load\$3 same	USPAT;	2003/10/16 10:20
		manag\$5)) and ((ping-pong (ping adj2 pong)) with memory)) not	US-PGPUB;	
		(((((real-time (real adj2 time)) same (dsp (digital adj2 signal adj2	ЕРО; ЛРО;	
		processor)) same load\$3 same manag\$5)) and ((ping-pong (ping adj2	DERWENT;	
		pong)) with memory))	IBM_TDB	2003/10/16 10:53
12	134	(dsp (digital adj2 signal adj2 processor)) and ((ping-pong (ping adj2	USPAT; US-PGPUB;	2003/10/10 10.53
		pong)) with memory)	EPO; JPO;	
			DERWENT;	
			IBM_TDB	
13	0	((((dma (direct adj2 memory adj2 access)) same register\$3) and (fifo	USPAT;	2003/10/16 11:06
13		same (state adj2 machine)) and (memor\$3 same interfac\$3 same (dma	US-PGPUB;	
		(direct adj2 memory adj2 access)) and external) and (((cpu processor)	ЕРО; ЛРО;	
		same ((control\$4 near4 register) and (status near4 register) and (dma	DERWENT;	
		(direct adj2 memory adj2 access))) and (dsp (digital adj2 signal adj2	IBM_TDB	
		processor)))))) and ((ping-pong (ping adj2 pong)) with buffer\$3)		
14	1	((((dma (direct adj2 memory adj2 access)) same register\$3) and (fifo	USPAT;	2003/10/16 10:52
		same (state adj2 machine)) and (memor\$3 same interfac\$3 same (dma	US-PGPUB;	
		(direct adj2 memory adj2 access)) and external) and (((cpu processor)	ЕРО; ЛРО;	
		same ((control\$4 near4 register) and (status near4 register) and (dma	DERWENT;	
		(direct adj2 memory adj2 access))) and (dsp (digital adj2 signal adj2	IBM_TDB	
1.5	1	processor)))))) and (thread\$3 with memory)	I ISDAT:	2003/10/16 13:20
15	44	1 (((1 (0) 1))	USPAT; US-PGPUB;	2003/10/10 13:20
) and (thread\$3 with memory)	ЕРО; ЛРО;	
			DERWENT;	
			IBM TDB	
			םמו ואומו	

22	7	(dsp (digital adj2 signal adj2 processor)) and (MUX near5 (external	USPAT;	2003/10/16 11:16
		near5 memory))	US-PGPUB;	
			EPO; JPO; DERWENT;	
			IBM_TDB	
28	241	(dsp (digital adj2 signal adj2 processor)) and (MUX near5 memory)	USPAT;	2003/10/16 11:19
		(uop (u.g.m. uuj2 u.g.m. uuj2 provosoo)) uud (u.e.a. u.e.a. u.e.a.)	US-PGPUB;	
			ЕРО; ЛРО;	
			DERWENT;	
			IBM_TDB	
29	138	(dsp (digital adj2 signal adj2 processor)) and (MUX same memory	USPAT;	2003/10/16 11:20
		same external)	US-PGPUB;	
			EPO; JPO;	
			DERWENT; IBM_TDB	
30	27	((((dsp (digital adj2 signal adj2 processor)) same load\$3 same	USPAT;	2003/10/16 11:26
1 30	2'	manag\$5)) and (thread\$3 with memory)) and (mux (external same	US-PGPUB;	2003/10/10 11:20
		memory same ((memory with access with controller with array) mac)))	ЕРО; ЛРО;	
			DERWENT;	
			IBM_TDB	
31	27	((((dsp (digital adj2 signal adj2 processor)) same load\$3 same	USPAT;	2003/10/16 14:00
		manag\$5)) and (thread\$3 with memory)) and (mac ((memory with	US-PGPUB;	
		access with control\$4) same array) or (memory with (control\$4	ЕРО; ЛРО;	
1		manag\$5) same external))	DERWENT;	
34	26	((((dsp (digital adj2 signal adj2 processor)) same load\$3 same	IBM_TDB USPAT;	2003/10/16 14:25
34	20	manag\$5)) and (thread\$3 with memory)) and ((mac (memory with	US-PGPUB;	2003/10/10 14.23
		access with control\$4)) and external)	ЕРО; ЛРО;	
			DERWENT;	
			IBM_TDB	
35	33	(((real-time (real adj2 time)) same (dsp (digital adj2 signal adj2	USPAT;	2003/10/16 14:25
	ŀ	processor)) same load\$3 same manag\$5)) and ((mac (memory with	US-PGPUB;	
		access with control\$4)) and external)	ЕРО; ЛРО;	
	!		DERWENT;	
	10	(parallel\$3 same (cpu processor processing) and (dsp (digital adj2 signal	IBM_TDB USPAT;	2003/10/14 14:45
-	10	adj2 processor)) and (load\$3 near3 manag\$5) and communicat\$3 and	US-PGPUB;	2003/10/14 14.43
		transmi\$5 and (cpu processor) and ((dma (direct adj2 memory adj2	ЕРО; ЛРО;	
		access)) same register\$3) and fifo and (state adj2 machine) and	DERWENT;	
		(memor\$3 same interfac\$3 same (dma (direct adj2 memory adj2 access))	IBM_TDB	
		same external) and (control\$4 near4 register) and (status near4 register)		
		and (dma (direct adj2 memory adj2 access)))		
-	10	((parallel\$3 same (cpu processor processing)) and (dsp (digital adj2	USPAT;	2003/10/14 14:58
		signal adj2 processor)) and (load\$3 near3 manag\$5) and communicat\$3 and transmi\$5 and (cpu processor) and ((dma (direct adj2 memory adj2	US-PGPUB; EPO; JPO;	
		and transmiss and (cpu processor) and (dana (direct adj2 memory adj2 access)) same register\$3) and fifo and (state adj2 machine) and	DERWENT;	
		(memor\$3 same interfac\$3 same (dma (direct adj2 memory adj2 access))	IBM TDB	
		same external) and (control\$4 near4 register) and (status near4 register)		
		and (dma (direct adj2 memory adj2 access)))		
-	12	((parallel\$3 same (cpu processor processing)) and (dsp (digital adj2	USPAT;	2003/10/14 15:06
		signal adj2 processor)) and (load\$3 near3 manag\$5) and communicat\$3	US-PGPUB;	
		and transmi\$5 and (cpu processor) and ((dma (direct adj2 memory adj2	ЕРО; ЛРО;	
		access)) and register\$3) and fifo and (state adj2 machine) and (memor\$3	DERWENT;	
		same interfac\$3 same (dma (direct adj2 memory adj2 access)) and external) and (control\$4 near4 register) and (status near4 register) and	IBM_TDB	
		(dma (direct adj2 memory adj2 access)))		
	12	((((parallel\$3 same (cpu processor processing)) and (dsp (digital adj2	USPAT;	2003/10/14 14:59
	'-	signal adj2 processor)) and (load\$3 near3 manag\$5) and communicat\$3	US-PGPUB;	
		and transmi\$5 and (cpu processor) and ((dma (direct adj2 memory adj2	ЕРО; ЈРО;	
		access)) and register\$3) and fifo and (state adj2 machine) and (memor\$3	DERWENT,	
		same interfac\$3 same (dma (direct adj2 memory adj2 access)) and	IBM_TDB	
		external) and (control\$4 near4 register) and (status near4 register) and		
L		(dma (direct adj2 memory adj2 access)))) not "110"	<u> </u>	L

-	2	((((parallel\$3 same (cpu processor processing)) and (dsp (digital adj2 signal adj2 processor)) and (load\$3 near3 manag\$5) and communicat\$3 and transmi\$5 and (cpu processor) and ((dma (direct adj2 memory adj2 access)) and register\$3) and fifo and (state adj2 machine) and (memor\$3 same interfac\$3 same (dma (direct adj2 memory adj2 access)) and external) and (control\$4 near4 register) and (status near4 register) and (dma (direct adj2 memory adj2 access)))) not (((parallel\$3 same (cpu processor processing))) and (dsp (digital adj2 signal adj2 processor)) and	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/14 14:59
		(load\$3 near3 manag\$5) and communicat\$3 and transmi\$5 and (cpu processor) and ((dma (direct adj2 memory adj2 access)) same register\$3) and fifo and (state adj2 machine) and (memor\$3 same interfac\$3 same (dma (direct adj2 memory adj2 access)) same external) and (control\$4 near4 register) and (status near4 register) and (dma (direct adj2 memory adj2 access))))		
-	52	(((dma (direct adj2 memory adj2 access)) same register\$3) and (fifo same (state adj2 machine)) and (memor\$3 same interfac\$3 same (dma (direct adj2 memory adj2 access)) and external) and (((cpu processor) same ((control\$4 near4 register) and (status near4 register) and (dma (direct adj2 memory adj2 access))) and (dsp (digital adj2 signal adj2 processor))))))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/15 08:01
-	48	((real-time (real adj2 time)) same (dsp (digital adj2 signal adj2 processor)) same load\$3 same manag\$5)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/16 10:19
-	8	(((real-time (real adj2 time)) same (dsp (digital adj2 signal adj2 processor)) same load\$3 same manag\$5)) and ((ip and (sonet (synchronous with optical with network))) (connect\$3 same (fiber-optic (fiber with optic)) same transmi\$5))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/15 10:41
-	1	((((dma (direct adj2 memory adj2 access)) same register\$3) and (fifo same (state adj2 machine)) and (memor\$3 same interfac\$3 same (dma (direct adj2 memory adj2 access)) and external) and (((cpu processor) same ((control\$4 near4 register) and (status near4 register) and (dma (direct adj2 memory adj2 access))) and (dsp (digital adj2 signal adj2 processor)))))) and ((ip and (sonet (synchronous with optical with network))) (connect\$3 same (fiber-optic (fiber with optic)) same transmi\$5))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/15 10:42
-	271	(dsp (digital adj2 signal adj2 processor)) and ((ip and (sonet (synchronous with optical with network))))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/15 10:43
-	37	(dsp (digital adj2 signal adj2 processor)) and ((ip and (sonet (synchronous with optical with network))) and (connect\$3 same (fiber-optic (fiber with optic)) same transmi\$5))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/15 10:59
-	1	((dsp (digital adj2 signal adj2 processor)) and ((ip and (sonet (synchronous with optical with network))) (connect\$3 same (fiber-optic (fiber with optic)) same transmi\$5))) and foip	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/15 11:00
-	1	(((dsp (digital adj2 signal adj2 processor)) and ((ip and (sonet (synchronous with optical with network))) (connect\$3 same (fiber-optic (fiber with optic)) same transmi\$5))) and foip) and (transmi\$5 communicat\$3 data medium)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/15 11:01
-	52	(((dma (direct adj2 memory adj2 access)) same register\$3) and (fifo same (state adj2 machine)) and (memor\$3 same interfac\$3 same (dma (direct adj2 memory adj2 access)) and external) and (((cpu processor) same ((control\$4 near4 register) and (status near4 register) and (dma (direct adj2 memory adj2 access))) and (dsp (digital adj2 signal adj2 processor))))))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/16 10:16